



Substitute Form PTO-1449 (Modified) Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-597001	Application No. 09/941,952
	Applicant William R. Wheeler et al.		
	Filing Date August 29, 2001	Group Art Unit 2123	

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
M.S.	AA	RE 38,059	04/2003	Yano, et al.			RECEIVED MAR 17 2004 Technology Center 2100
	AB	4,703,435	10/1987	Darringer, et al.			
	AC	4,970,664	11/1990	Kaiser, et al.			
	AD	5,212,650	05/1993	Hooper, et al.			
	AE	5,267,175	11/1993	Hooper			
	AF	5,278,769	01/1994	Bair, et al.			
	AG	5,287,289	02/1994	Kageyama, et al.			
	AH	5,297,053	03/1994	Pease, et al.			
	AI	5,301,318	04/1994	Mittal			
	AJ	5,384,710	01/1995	Lam, et al.			
	AK	5,475,605	12/1995	Lin			
	AL	5,493,507	02/1996	Shinde, et al.			
	AM	5,544,067	08/1996	Rostoker, et al.			
	AN	5,568,397	10/1996	Yamashita, et al.			
	AO	5,598,347	01/1997	Iwasaki			
	AP	5,603,015	02/1997	Kurosawa, et al.			
	AQ	5,604,894	02/1997	Pickens, et al.			
	AR	5,663,662	09/1997	Kurosawa			
	AS	5,673,198	09/1997	Lawman, et al.			
	AT	5,685,006	11/1997	Shiraishi			
	AU	5,694,579	12/1997	Razdan, et al.			
	AV	5,706,476	01/1998	Giramma			
	AW	5,717,928	02/1998	Campmas, et al.			
	AX	5,724,250	03/1998	Kerzman, et al.			
	AY	5,757,655	05/1998	Shih, et al.			
	AZ	5,809,283	09/1998	Vaidyanathan, et al.			
✓	AAA	5,831,869	11/1998	Ellis, et al.			

Examiner Signature <i>William Wheeler</i>	Date Considered <i>1/9/05</i>
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Sheet 2 of 4

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Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
M.S.	ABB	5,841,663	11/1998	Sharma, et al.			
	ACC	5,892,682	04/1999	Hasley, et al.			
	ADD	5,903,469	05/1999	Ho			
	AEE	5,937,190	08/1999	Gregory, et al.			
	AFF	5,974,242	10/1999	Damarla, et al.			
	AGG	6,077,304	06/2000	Kasuya			
	AHH	6,161,211	12/2000	Southgate			
	AII	6,178,541	01/2001	Joly, et al.			
	AJJ	6,208,954	03/2001	Houtchens			
	AKK	6,216,256	04/2001	Inoue, et al.			
	ALL	6,226,780	05/2001	Bahra, et al.			
	AMM	6,234,658	05/2001	Houldsworth			
	ANN	6,275,973	08/2001	Wein			
	AOO	6,292,931	09/2001	Dupenloup			
	APP	6,327,693	12/2001	Cheng, et al.			
	AQQ	6,353,806	03/2002	Gehlot			
	ARR	6,353,915	03/2002	Deal, et al.			
	ASS	6,360,356	03/2002	Eng			
	ATT	6,381,563	04/2002	O'Riordan, et al.			
	AUU	6,381,565	04/2002	Nakamura			
	AVV	6,421,816	07/2002	Ishikura			
	AWW	6,438,729	08/2002	Ho			
	AXX	6,438,731	08/2002	Segal			
	AYY	6,440,780	08/2002	Kimura, et al.			
	AZZ	6,473,885	10/2002	Wallace			
	AAAA	6,477,688	11/2002	Wallace			
	ABBB	6,490,545	12/2002	Peng			

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Sheet 3 of 4Substitute Form PTO-1449
(Modified)U.S. Department of Commerce
Patent and Trademark OfficeAttorney's Docket No.
10559-597001Application No.
09/941,952**Information Disclosure Statement
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(Use several sheets if necessary)

(37 CFR §1.98(b))

Applicant
William R. Wheeler et al.Filing Date
August 29, 2001Group Art Unit
2123**U.S. Patent Documents**

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
M.S.	ACCC	6,505,328	01/2003	Van Ginneken, et al.			
	ADDD	6,516,456	02/2003	Garnett, et al.			
	AEEE	6,519,742	02/2003	Falk			
	AFFF	6,523,156	02/2003	Cirit			
	AGGG	6,539,536	03/2003	Singh et al.			
	AHHH	6,546,528	04/2003	Sasaki, et al.			
	AIII	6,574,787	06/2003	Anderson			
	AJJJ	6,591,407	07/2003	Kaufman, et al.			
	AKKK	2002/0038447	03/2002	Kim, et al.			
	ALLL	2002/0059054	05/2002	Bade, et al.			
	AMMM	2002/0112221	08/2002	Ferreri, et al.			
	ANNN	2002/0138244	09/2002	Meyer			
	AOOO	2003/0004699	01/2003	Choi, et al.			
	APPP	2003/0036871	02/2003	Fuller, et al.			
	AQQQ	2003/0177455	09/2003	Kaufman, et al.			

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Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
M.S.	ARRR	0 404 482	12/1990	EPO				
	ASSS	0 720 233	07/1996	EPO				
	ATTT	58-060559	04/1983	Japan				
	AUUU	03-225523	10/1991	Japan				
	AVVV	07-049890	02/1995	Japan				
	AWWW	08-314892	11/1996	Japan				
	AXXX	2001-068994	03/2001	Japan				
	AYYY	WO 98/37475	08/1998	WIPO				
	AZZZ	WO 98/55879	12/1998	WIPO				

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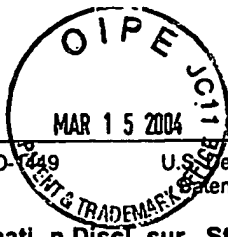
M. S. Shuman

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		Applicant William R. Wheeler et al.	
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(37 CFR §1.98(b))

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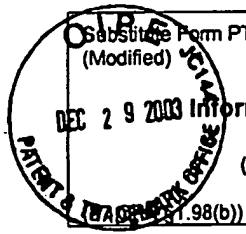
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Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
M.S.	AAAAA	WO 99/39268	08/1999	WIPO				
M.S.	ABBBB	WO 00/65492	11/2000	WIPO				

Other Documents (include Author, Title, Date, and Place of Publication)		
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M.S.	ACCCC	Gassenfeit, E. H., "Control System Design Realization via VHDL-A: Requirements", Proceedings of the 1996 IEEE International Symposium on Computer-Aided Control System Design, September 15, 1996, pp. 282-285.
	ADDDD	Kutzschebauch, "Efficient logic optimization using regularity extraction", Proceedings of 2000 International Conference on Computer Design, September 17, 2000, pp. 487-493.
	AEEEE	Lahti, et al., "SADE: a Graphical Toll for VHDL-Based System Analysis", 1991 IEEE International Conference on Computer-Aided Design, November 11, 1991, pp. 262-265.
	AFFFF	Lin, et al., "A Goal Tree Based High-Level Test Planning System for DSP Real Number Models", 1998 Proceedings of International Test Conference, October 18, 1998, pp. 1000-1009.
	AGGGG	NB84035598, "Binary Relational Schema to Model Structured LSI Design", IBM Technical Disclosure Bulletin, Vol. 26, No. 10B, March 1984, pp. 5598-5601.
	AHHHH	NN7807629, "Functional Oriented Symbolic Macromodeling Algorithm", IBM Technical Disclosure Bulletin, Vol. 21, No. 2, July 1978, pp. 629-631.
	AIIII	NN8006341, "Macro Physical-To-Logical Checking LSI Chip Design", IBM Technical Disclosure Bulletin, Vol. 23, No. 1, June 1980, pp. 341-345.
	AJJJJ	NN9407481, "Functional Modeling using object Collaboration Diagram", IBM Technical Disclosure Bulletin, Vol. 37, No. 7, July 1994, pp. 481-486.
	AKKKK	Parlakkbilek, et al., "A Multiple-Strength Multiple-Delay Compiled-Code Logic Simulator", <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , 12(12):1937-1946 (1993).
	ALLLL	Su, Stephen, "An Interactive Design Automation System", <i>Proceedings of the 10th Design Automation Workshop on Design Automation</i> , pp. 253-261, June 1973
	AMMMM	Yli-Pietila, et al., "The Design and Simulation of Complex Multitechnology Systems", <i>IEEE International Conference on Systems Engineering</i> , August 9, 1990, pp. 474-477.

Examiner Signature <i>M. S. Wheeler</i>	Date Considered 1/9/05
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Patent and Trademark Office

 Attorney's Docket No.
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William R. Wheeler et al.

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 Group Art Unit
2123

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Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
M.S.	AA	5,220,512	06/1993	Watkins, et al.			
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	AJ						
	AK						

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Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
M.S.	AL	0 433 066	06/1991	EPO				
M.S.	AM	0 901 088	03/1999	EPO				
M.S.	AN	1 065 611	01/2001	EPO				
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	AP							

Other Documents (include Author, Title, Date, and Place of Publication)

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	AS	
	AT	

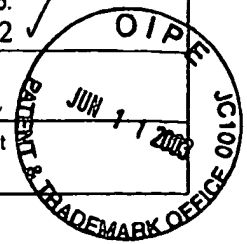
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U.S. Patent Documents							
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M.S.	AA	2002/0049957	04/2002	Hosono et al.			
	AB	6,233,723	05/2001	Pribetich			
	AC	5,828,581	10/1998	Matumura			
	AD	2001/0018758	08/2001	Tanaka et al.			
	AE	5,666,289	09/1997	Watkins			
	AF	6,066,179	05/2000	Allan			
	AG	5,892,678	04/1999	Tokunoh et al.			
	AH	6,120,549	09/2000	Goslin et al.			
	AI	5,513,119	04/1996	Moore et al.			
	AJ	6,477,683	11/2002	Killian et al.			
	AK	6,505,341	01/2003	Harris et al.			
	AL	2003/0005396	01/2003	Chen et al.			
	AM	5,553,002	09/1996	Dangelo et al.			
	AN	6,366,874	04/2002	Lee et al.			
	AO	6,044,211	03/2000	Jain			
	AP	5,258,919	11/1993	Yamanouchi et al.			
	AQ	5,220,512	06/1993	Watkins et al.			
	AR	6,324,678	11/2001	Dangelo et al.			
	AS	6,236,956	05/2001	Mantooth et al.			
	AT	6,132,109	10/2000	Gregory et al.			
	AU	5,963,724	10/1999	Mantooth et al.			
	AV	5,852,564	12/1998	King et al.			
	AW	6,106,568	08/2000	Beausang et al.			
	AX	6,219,822	04/2001	Griestede et al.			
	AY	5,128,871	07/1992	Schmitz			
	AZ	5,506,788	04/1996	Cheng et al.			
	AAA	6,457,164	09/2002	Hwang et al.			

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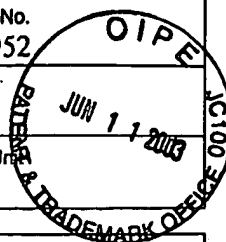
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U.S. Patent Documents							
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M. S.	ABB	2002/0042904	04/2002	Ito et al.			
	ACC	6,449,762	09/2002	McElvain			
	ADD	6,378,115	04/2002	Sakurai			
	AEE	6,272,671	08/2001	Fakhry			
	AFF	6,260,179	07/2001	Ohsawa et al.			
	AGG	6,117,183	09/2000	Teranishi et al.			
	AHH	5,889,677	03/1999	Yasuda et al.			
	AII	6,298,468	10/2001	Zhen			
	AJJ	6,477,689	11/2002	Mandell et al.			
	AKK	6,480,985	11/2002	Reynolds et al.			
	ALL	6,519,755	02/2003	Anderson			
	AMM	6,152,612	11/2000	Liao et al.			
	ANN	2002/0023256	02/2002	Seawright			
	AOO	6,053,947	04/2000	Parson			
	APP	6,135,647	10/2000	Balakrishnan et al.			
	AQQ	2003/0016246	01/2003	Singh			
	ARR	2003/0016206	01/2003	Taitel			
	ASS	6,233,540	05/2001	Schaumont et al.			
	ATT	6,487,698	11/2002	Andreev et al.			
	AUU	6,311,309	10/30/2001	Southgate			
	AVV	2002/0166100	11-2002	Meding			
	AWW	US 5,629,857	05-1997	Brennan			
	AXX	2002/0046386	04-2002	Skoll et al.			
	AYY	6,401,230	06/2002	Ahanessians et al.			
	AZZ	5,933,356	08/1999	Rostoker et al.			
	AAAA	6,205,573	03/2001	Hasegawa			
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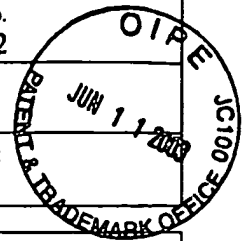
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							Yes	No
	ADDD							
	AEEE							
	AFFF							

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
M.S.	AGGG	Foley et al., "An Object Based Graphical User Interface for Power Systems", IEEE Transactions on Power Systems, Vol. 8, No. 1, February 1993, pp. 97-104.
	AHHH	Pedram et al., "Floorplanning with Pin assignment", 1990 IEEE International Conference on Computer-Aided Design, 11 November 1990, pp. 98-101.
	AIII	Renoir, HDL Design Datasheet, Mentor Graphics, 1-8, 1999.
	AJJJ	Mentor Graphics Corporation, Renoir™ With HDL2Graphics™, pp. 1-6, 1998, Oregon.
	AKKK	Mentor Graphics Corporation, Renoir HDL Design Datasheet, pp. 1-2, 1999, Oregon.
	ALLL	Computer Design, "AFTER HARD KNOCKS, CYCLE-BASED SIMULATORS STAND THEIR GROUND". http://www.computer-design.com/Editorial/1996/10/ASIC/after.html , accessed on August 23, 2001, pages 1-5.
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